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REMARKS

Applicants appreciate the Examiner's thorough examination of the present application as evidenced by the final Office Action of February 6, 2006 (hereinafter "Final Action"). In response, Applicants have amended independent Claims 6, 21, and 32 to clarify that the cryptographic processor is a cryptographic accelerator processor and that the local memory is exclusively associated with the cryptographic accelerator processor. Applicants have also amended independent Claims 1, 16, and 27 to clarify that the local memory is exclusively associated with the cryptographic processor. Applicants respectfully submit that the cited reference fails to disclose or suggest, at least, the recitations of the independent claims as amended. Therefore, Applicants respectfully submit that all pending claims are in condition for allowance. Favorable reconsideration of all pending claims is respectfully requested for at least the reasons discussed hereafter.

Independent Claims 6, 21, and 32 are Patentable

Claims 6, 21, and 32 stand rejected under 35 U.S.C. §102(e) as being anticipated by U. S. Patent No. 6,327,652 to England et al. (hereinafter "England"). (Final Action, page 3). Independent Claims 6, 21, and 32 have been amended to clarify that the cryptographic processor is a cryptographic accelerator processor and that the local memory is exclusively associated with the cryptographic accelerator processor. As acknowledged in the Final Action on page 2, the processor 160 shown in FIG. 1B of England uses both the nonvolatile memory 142 and the volatile memory 144. Thus, England does not disclose or suggest a local memory that is exclusively associated with the cryptographic accelerator processor 162 of FIG. 1B.

For at least these reasons, Applicants respectfully submit that Claims 6, 21, and 32 are patentable over the cited reference.

Independent Claims 1, 16, and 27 are Patentable

Claims 1, 16, and 27 stand rejected under 35 U.S.C. §102(e) as being anticipated by England. (Final Action, page 2). Independent Claim 1 is directed to a method of operating a cryptographic data processing system and recites, in part:

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cryptographic processor.

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loading at least one operand from the system memory to the local memory; and

executing an instruction using the cryptographic processor that references the at least one operand using a first relative position in the local memory, comprising:

generating a result based on the at least one operand; and storing the result at a second relative position in the local memory; wherein the first relative position comprises a first offset from a base address in the local memory, and the second relative position comprises a second offset from the base address in the local memory; and wherein the local memory is exclusively associated with the

Claims 16 and 27 include similar recitations. Thus, according to independent Claim 1, the

local memory is exclusively associated with the cryptographic processor.

With reference to FIG. 1B of England, the Final Action cites the nonvolatile memory 142 as corresponding to the system memory and the volatile memory 144 as corresponding to the local memory elements of Claim 1. (Final Action, page 2). As discussed above with reference to Claims 6, 21, and 32, however, the volatile memory 144 is not exclusively associated with the cryptographic accelerator 162. Instead, as acknowledged in the Final Action on page 2, the processor 160 uses the volatile memory 144.

For at least these reasons, the Applicants respectfully submit that Claims 1, 16, and 27 are patentable over the cited reference and that dependent Claims 2 - 4, 17 - 18, and 28 - 29 are patentable for at least the reason that they depend from an allowable claim.

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CONCLUSION

In light of the above amendments and remarks, Applicants respectfully submit that the above-entitled application is now in condition for allowance. Favorable reconsideration of this application, as amended, is respectfully requested. If, in the opinion of the Examiner, a telephonic conference would expedite the examination of this matter, the Examiner is invited to call the undersigned attorney at (919) 854-1400.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on April 28, 2006.

Traci A. Brown